QUANTINUUM

Quantinuum System Model H1 Emulator Product Data Sheet

Version 6.0 December 1, 2022

TABLE OF CONTENTS

INTRODUCTION

This Product Data Sheet covers all features and characteristics of the **Quantinuum System Model H1 Emulator**, which includes the H1-1 and H1-2 emulators.

FEATURES

- High fidelity noise models and parameters closely mimicking System Model H1 (hardware) performance. Each emulator uses the same physical noise model, but noise rates reflect performance of the device being emulated.
- Uses identical API for job submission as System Model H1, enabling seamless translation from emulator to hardware
- Uses identical compiler as System Model H1, containing all the native gates (single-qubit rotations, two-qubit ZZ gates, arbitrary angle ZZ gates), transport operations and classical operations used in System Model H1
- Provides identical output format as System Model H1
- Allows usage of unique System Model H1 attributes: all-to-all connectivity and qubit reuse after mid-circuit measurement
- Available even while System Model H1 is offline to enable maximized productivity and development time

USE CASES

The System Model H1 Emulator provides a high-fidelity emulation of System Model H1. Use cases include:

- Debugging of quantum code before running on physical hardware
- Optimization of quantum code in the presence of noise mechanisms
- Exploring new algorithms and techniques for quantum error correction
- Introduction to System Model H1 and its unique differentiating capabilities such as qubit reuse after mid-circuit measurement, all-to-all connectivity, and high-fidelity gates

FUNCTIONAL REQUIREMENTS

The System Model H1 Emulator is meant to be a functional emulation of System Model H1 and therefore supports the same functional operations as H1. Specifically, the System Model H1 Emulator supports:

- OPENQASM 2.0 circuits
- Quantinuum QASM enhancements, including classical logic, math, and program flow control
- Quantinuum native gate set[1] – $R_z(\lambda)$, $U_{1q}(\theta, \varphi)$, ZZ, $RZZ(\theta)$
- Common compound gates from OPENQASM library, e.g., CX, H
- User-defined compound gates
- User option of noiseless simulation or inclusion of System Model H1 noise models
- Large quantum circuits with a limit of 10,000 on the number of shots
- Identical queuing prioritization as System Model H1

---

[1] For definition of native gates, please request a copy of the *Quantinuum System Model H1 Product Data Sheet*

EMULATOR ACCESS AND OUTPUT

Communication with the System Model H1 Emulator occurs through an API endpoint based on the OpenQASM 2.0 standard (Cross, Lev, John, & Jay, 2017). Interface details are given in the *Quantinuum Application Programming Interface (API) Specification.*

Users can select a System Model H1 Emulator in the machine list API, designated with the "E" suffix machine name. The output of H1 Emulator is a JSON-formatted array, identical to the output format of System Model H1. Through the Job Submission API, users may select the type of emulator used and turning on or off the application of the error model.

PERFORMANCE

The performance of the System Model H1 Emulator is measured in the fidelity to hardware. With inclusion of accurate and up-to-date noise models, the System H1 Emulator can provide a high-fidelity representation of System Model H1 output. Fidelity is verified at Quantinuum by comparison between the emulator and hardware outputs. However, noise models cannot fully capture the behavior of System Model H1; users should expect some variance. In the case of exceptional or unexplained variance, users should contact Quantinuum technical support at QCsupport@quantinuum.com to discuss the circuit and results.

EMULATION METHOD

The System Model H1 Emulator, accessible via the API, receives instructions directly from the same compilers used by the System Model H1 physical quantum hardware. These compilers translate the submitted quantum program into a set of instructions comprising of the native gate operations and the transport operations necessary to reconfigure the ion chain at each step of the program.

Users can choose between either a state-vector or stabilizer emulation method; in both cases results are performed shot-by-shot. The state-vector emulation method can run any general quantum circuits, while the stabilizer emulation method is restricted to circuits involving only quantum unitary gates that are Clifford operations.

The error model for the emulation can be turned on or off, allowing noisy or noise-free emulations, respectively. The emulated error model includes:

- depolarizing gate noise
- leakage errors, crosstalk noise
- dephasing noise due to transport and qubit idling

Except for dephasing, errors on physical qubits are modeled as stochastic processes. For the state-vector emulation, dephasing is handled as a coherent Z rotation according to a dephasing rate and the duration the qubit spends in transport or while idling while other qubits are being gated. For the stabilizer emulation, the dephasing noise is treated as a stochastic Z error where the probability of a Z error is equal to the Pauli twirled approximation of the coherent dephasing channel, which is proportional to the square of the dephasing rate multiplied by the duration.

NOISE MODEL

Users who have direct access to the Quantinuum API have the option of experimenting with the physical noise parameters of the emulator. When deviating from the default emulation model, users should not assume that performance predicted with modified error parameters will match hardware performance.

All parameters listed in Table 1 are the default settings of the System Model H1 emulators. As updates to the System Model H1 quantum computers are made, the emulator noise parameters and the underlying error model are subject to change to accommodate performance improvements, updates in the methodology for measuring devices parameter and research into the noise sources themselves

All the errors are applied even when only certain parameters are specified. Only the parameters specified are overridden. To turn off certain error parameters, explicitly set them to 0.

For more information on the errors observed, see the following publications: Realization of Real-Time Fault-Tolerant Quantum Error Correction, Implementing Fault-tolerant Entangling Gates on the Five-qubit Code and the Color Code.

Table 1 Default Settings of the System Model H1 Emulators

Default Settings	H1-1	H1-2
General		
Qubits	20	12
Connectivity	All-to-all	All-to-all
Parallel two-qubit operations	5	3
Physical Noise		
Single-Qubit Fault Probability (p1)	3.84×10^{-5}	3.408×10^{-5}
Two-Qubit Fault Probability (p2)	2.416×10^{-3}	2.45×10^{-3}
Bit Flip Measurement Probability (0 outcome) (p_meas)	1.3×10^{-3}	1.833×10^{-3}
Bit Flip Measurement Probability (1 outcome) (p_meas)	4.23×10^{-3}	4.6×10^{-3}
Crosstalk Measurement Fault Probability (p_crosstalk_meas)	1.202×10^{-5}	5.273×10^{-5}
Initialization Fault Probability (p_init)	3.62×10^{-5}	5×10^{-6}
Crosstalk Initialization Probability (p_crosstalk_init)	3.62×10^{-5}	2×10^{-5}
Single-Qubit Spontaneous Emission (p1_emission)	6.4×10^{-6}	6.4×10^{-6}
Single-Qubit Spontaneous Emission in Two-Qubit Gate (p2_emission)	2.77×10^{-4}	2.77×10^{-4}
Dephasing Noise		
Coherent Dephasing Rate (coherent_dephasing_rate)	0.172	0.17
Incoherent Dephasing Rate (incoherent_dephasing_rate)	0.354	0.35
Arbitrary Angle Noise Scaling		

Fit Parameter 1 (przz_a)	1.09	1.09
Fit Parameter 2 (przz_b)	0.05	0.05
Fit Parameter 3 (przz_c)	1.36	1.36
Fit Parameter 4 (przz_d)	0.03	0.03

Physical Noise

The emulator runs with default error parameters that represent a noise environment that closely resembles the respective hardware. These error parameters can be set and used to override the default error parameters and do finer-grain tweaks of the error model. Modification of the error parameters away from default values is an advanced option and not recommended as a starting point for emulations of hardware performance.

- **Single-Qubit Fault Probability (p1):** probability of a fault occuring during a single-qubit gate
- **Two-Qubit Fault Probability (p2):** probability of a fault occuring during a two-qubit gate
- **Bit Flip Measurement Probability (p_meas):** probability of a bit flip being applied to a measurement. Either a float or a tuple of 2 floats. If it is a single float then that error rate is used to bitflip both 0 and 1 measurement results. If a tuple is supplied, the first element is the probability a bit flip is applied if a 0 result occurs during measurement while the second error rate if a 1 is measured.
- **Crosstalk Measurement Fault Probability (p_crosstalk_meas):** probability of a crosstalk measurement fault occurring
- **Initialization Fault Probability (p_init):** probability of a fault occuring during initialization of a qubit
- **Crosstalk Initiailization Fault Probability (p_crosstalk_init):** probability of a cross-talk fault occurring during initialization of a qubit
- **Single-Qubit Spontaneous Emission (p1_emission):** spontaneous emission for a single qubit
- **Single-Qubit Spontaneous Emission in Two-Qubit Gate (p2_emission):** spontaneous emission for a single qubit in a two-qubit gate

The single and two-qubit fault probabilities are largely modeled using depolarizing channels; however, there is smaller probability that a spontaneous emission event happens. The probability is about an order of magnitude lower than the corresponding depolarizing error rate. The spontaneous emission error rates can be scaled using the scaling parameters given in the Scaling section. If a spontaneous emission event happens then ¼ the time X is applied, ¼ the time Y is applied, and ½ the time leakage is applied. For more details see: Realization of Real-Time Fault-Tolerant Quantum Error Correction.

The two-qubit fault probability corresponds to the depolarizing probability of the System Model H1 fully entangling two-qubit gate, $ZZ()$. The probability of depolarizing error for the arbitrary angle two-qubit gate, $RZZ(\theta)$, depends on the angle θ. The spontaneous emission error channel is the same for both $ZZ()$ and $RZZ(\theta)$.

The noise model includes a memory error for which rotations about Z are applied. This is often called "dephasing" or "memory" noise. Coherent modeling of this noise is applied by default for the state-vector simulator. For coherent dephasing noise modeling, the RZ gate (frequency x duration) is applied during transport and qubit idling. Incoherent modeling is applied by default for the stabilizer simulator. For incoherent dephasing noise modeling, Pauli Z is applied during transport and qubit idling according to the probability $\sin(\text{frequency x duration}/2)^2$.

Two different frequencies in the error model *coherent dephasing rate* and *incoherent dephasing rate* are used for the coherent and incoherent noise modeling, respectively. Whether or not to use a coherent or incoherent simulation can be toggled with a *coherent dephasing* option, which is true by default for state-vector simulations (coherent_dephasing=True) and false by default for stabilizer simulations (coherent_dephasing=False). Note that since stabilizer simulators only simulate Clifford and measurement-like gates, the stabilizer simulator will not be able to apply the coherent dephasing noise model.

In addition, a transport dephasing parameter (transport_dephasing) is turned on by default and an idle dephasing parameter is turned on by defult (idle_dephasing). Both of these can be toggled off.

- **Coherent Dephasing Rate (coherent_dephasing_rate):** the gate RZ (frequency x duration) is applied during transport and qubit idling, applied by default for the state-vector simulator
- **Incoherent Dephasing Rate (incoherent_dephasing_rate):** Pauli Z is applied during transport and qubit idling according to the probability $\sin(\text{frequency x duration}/2)^2$, applied by default for the stabilizer simulator

Arbitrary Angle Noise Scaling

The System Model H1 systems have a native arbitrary-angle ZZ gate, $RZZ(\theta)$. For implementation of this gate in the System Model H1 Emulator, certain parameters relate to the strength of the depolarizing noise. These parameters depend on the angle θ. This is normalized so that $\theta = \frac{\pi}{2}$ gives the two-qubit fault probability (p2).

The parameters for depolarizing noise are fit parameters that fit the noise estimated as the angle θ changes per this equation:

$$\begin{array}{ll} \left(przz_a\sqrt{|\theta|/\pi} + przz_b\right) * p2 & \text{for } \theta < 0 \\ \left(przz_c\sqrt{|\theta|/\pi} + przz_d\right) * p2 & \text{for } \theta > 0 \\ 0 & \text{for } \theta = 0 \end{array}$$

- Fit Parameter 1 (przz_a)
- Fit Parameter 2 (przz_b)
- Fit Parameter 3 (przz_c)
- Fit Parameter 4 (przz_d)

Scaling

A scaling factor can be applied that multiplies all the default or supplied error parameters by the scaling rate. In this case, a 1 does not change the error rates while 0 makes all the errors have a probability of 0. Other aspects of the noise model can scale specific error rates in the error model, which include:

- **Scaling (scale):** scale all error rates in the model linearly
- **P1 Scaling (p1_scale):** scale the probability of single-qubit gates having a fault
- **P2 Scaling (p2_scale):** scale the probability of two-qubit gates having a fault
- **Measurement Scaling (meas_scale):** scale the probability of measurement having a fault
- **Initialization Scaling (init_scale):** scale the probability of initialization having a fault
- **Memory Scaling (memory_scale):** linearly scale the probability of dephasing causing a fault
- **Emission Scaling (emission_scale):** scale the probability that a spontaneous emission event happens during a single or two-qubit gate
- **Cross-talk Scaling (crosstalk_scale):** scale the probability that measurement or intialization crosstalk events get applied to qubits, during mid-circuit measurement and reset (initialization), "crosstalk" noise can occur that effectively measures other qubits in the trap or cause them to leak.
- **Leakage Scaling (leakage_scale):** scale the probability that a leakage even happens during single or two-qubit gates as well as during initalization or crosstalk; on the device half the time, spontaneous emission leads to a leakage event

APPENDIX

A H-System Quantum Credit (HQC) is defined as:

$$HQC = 5 + \frac{N_{1q} + 10\, N_{2q} + 5\, N_m}{5000} C$$

where N_{1q} is the number of single-qubit gates, N_{2q} is the number of native two-qubit gates, N_m is the number of state preparation and measurement operations in a circuit, including the initial implicit state preparation and any intermediate and final measurements and state resets, and C is the shot count. When a circuit is submitted (whether to the Syntax Checker, System Model H1, or System Model H1 Emulator) the cost in HQCs is returned with the results.